

IN THE CLAIMS

Claims 1-24 (canceled)

25. (currently amended) A server-on-a-board assembly comprising:

a computing device including a processor and a basic input/output system (BIOS);

an electronic data flash device detachably coupled to the computing device, the electronic data flash device including a flash memory device, a local control program, and one or more manual control buttons;

means for automatically coupling the local control program with the BIOS such that the local control program controls the computing device during a transfer of server image information from the flash memory device to the computing device, and then booting up the computing device using said server image information transferred from the flash memory device to the computing device such that the processor of the computing device is configured to function as a server according to the transferred server image information; and

means for controlling the processor of the computing device in response to actuation of the one or more control buttons.

26. (previously presented) The server-on-a-board assembly according to Claim 25, wherein said means for transferring comprises bus interface logic.

27. (canceled)

28. (previously presented) The server-on-a-board assembly according to Claim 25, wherein said means for controlling the processor further comprises means for shutting down the computing device in response to actuation of the one or more control buttons.

29. (previously presented) The server-on-a-board assembly according to Claim 25, wherein said means for controlling the processor of the computing device comprises means for restoring the computing device to a default state in response to actuation of a control button for a predetermined period of time.

30. (previously presented) The server-on-a-board assembly according to Claim 25, wherein said means for controlling the processor of the computing device comprises means for controlling one of a power shut down state and a power up state of the computing device in response to actuation of a control button for a predetermined period of time.

31. (previously presented) The server-on-a-board assembly according to Claim 25, wherein the electronic data flash device further comprises means for displaying status information associated with an operating state of the computing device.

32. (previously presented) The server-on-a-board assembly according to Claim 31, wherein said means for displaying status information comprises one of a light emitting diode (LED) and a liquid crystal display (LCD).

33. (previously presented) The server-on-a-board assembly according to Claim 25 wherein said electronic data flash device comprises a Universal Serial Bus device.

34. (previously presented) The server-on-a-board assembly according to Claim 25, wherein said electronic data flash device comprises at least one of an Express Card, a Mini PCI Express Card, a Secure Digital Card, a Multi Media Card, a Memory Stick Card and a Compact FLASH card.

35. (currently amended) A server-on-a-board assembly comprising:

a computing device including a basic input/output system (BIOS);

an electronic data flash device detachably coupled to the computing device, the electronic data flash device including a flash memory device, a local control program, and one or more display devices;

means for automatically coupling the local control program with the BIOS such that the local control program controls the computing device during a transfer of server image information from the flash memory device to the computing device, and then booting up the computing device using said server image information transferred from the flash memory device to the computing device, whereby the computing device is configured to function as a server according to the transferred server image information; and

means for transmitting status information from the computing device to the electronic data flash device and

for controlling said one or more display devices in accordance with the transmitted status information.

36. (previously presented) The server-on-a-board assembly according to Claim 35, wherein said one or more display devices comprises one of a light emitting diode (LED) and a liquid crystal display (LCD), and wherein said means for transmitting status information comprises means for actuating said one of said LED and said LCD in response to said status information.

37. (previously presented) The server-on-a-board assembly according to Claim 35, wherein said means for transferring comprises bus interface logic.

38. (canceled)

39. (currently amended) A server-on-a-board assembly comprising:

a computing device including a processor and a basic input/output system (BIOS);

an Universal Serial Bus (USB) device including a flash memory device, one or more of a light emitting diode (LED) device and a liquid crystal display (LCD) device, a local control program, and one or more control buttons;

means for automatically coupling the local control program with the BIOS such that the local control program controls the computing device during a transfer of server image information from the flash memory device to the computing device, and then booting up the computing device using said server image information transferred from the flash memory device to the computing device when the USB

device is coupled to the computing device, whereby the computing device is configured to function as a server according to the transferred server image information;

means for transmitting status information from the computing device to the USB device and for controlling said one of said LED and said LCD devices in accordance with the transmitted status information; and

means for controlling the processor of the computing device in response to actuation of the one or more control buttons.

40. (previously presented) The server-on-a-board assembly according to Claim 39, wherein said means for controlling the processor further comprises means for shutting down the computing device in response to actuation of the one or more control buttons.

41. (previously presented) The server-on-a-board assembly according to Claim 39, wherein said means for controlling the processor of the computing device comprises means for restoring the computing device to a default state in response to actuation of a control button for a predetermined period of time.

42. (previously presented) The server-on-a-board assembly according to Claim 39, wherein said means for controlling the processor of the computing device comprises means for controlling one of a power shut down state and a power up state of the computing device in response to actuation of a control button for a predetermined period of time.